

AMENDMENTS

In The Specification

Please amend paragraph [0034] as follows:

[0034] The present invention further provides a driving method for a light-emitting device as shown in Figure 7. In step 300, a video signal saves an image data into a buffer memory unit 124 with a speed of the first clock CLK1. The first clock CLK1 defines the clock of the frame such as 60Hz. In step 302, two clocks CLK2A and CLK2B are obtained by partition operation according to the second clock CLK2. The second clock CLK2 is a multiple of the first clock CLK1, for example. Preferably, the second clock CLK2 is double of the first clock CLK1. The frequencies of the ~~[[first and second clock CLK1 and CLK2]]~~two clocks CLK2A and CLK2B are the same as the first clock CLK1, while a delay exists between them. The delay is preferably $\frac{1}{2}$ of the frame. In step 304, a frame image data 204 is output at the clock CLK2A. Meanwhile, a discharged negative voltage 206 is also output at the clock CLK2A. The image data 203 corresponding to the frame can also be output at the clock CLK2B, while the discharged negative voltage 206 is output at the clock CLK2A.